

INTRODUCING ASYNCHRONOUS STRUCTURES IN THE “STRUCTURED ASIC” PLATFORMS

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Abstract: *The paper proposes the introduction of new hybrid concepts meant to improve the digital integrated circuits performance from the price/speed/power dissipation ratio point of view, by combining the advantages of asynchronous circuits (low dissipated power and easy interconnection) and advantages of „structured ASIC” platforms (time-to-market very short and low price per unit).*

Key words: *structured ASIC, GALs, asynchronous, dissipated power.*

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